<center>REMARKS</center>

This paper is responsive to a Non-Final Office action dated December 11, 2007. Claims 1-55 were examined. Claims 49, 50, 52, and 54-55 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 4,464,771 to Sorensen (hereinafter, "Sorensen"). Claims 51 and 53 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Sorensen in view of Applicants' Own Admitted Prior Art.

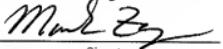Applicants appreciate the Examiner allowing claims 1-48.

Applicants have amended the specification to correct informalities noted in review. Accordingly, applicants respectfully request that the objection to the specification be reconsidered and withdrawn.

Claims 49, 50, 52, and 54-55 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 4,464,771 to Sorensen (hereinafter, "Sorensen"). Applicants have amended claim 49 to more clearly distinguish over Sorenson and have incorporated claim 50 into claim 49. Amended claim 49 recites *counting a number of data transitions in a received data stream that occur over a number of cycles of an oscillator to generate a count value; and inhibiting adjustment of the oscillator if the count value of the number of data transitions in the received data stream is below a predetermined threshold.* Sorenson only teaches detecting if a transition of the data occurs after the clock transition and if not, utilizes NAND gate 15 to prevent adjustment of the oscillator. Col. 4, lines 26-40. Sorenson does not count the transitions that occur over a number of cycles of the oscillator as claimed. Accordingly, applicants submit that claim 49 distinguishes over Sorenson and the other references of record.

Independent claims 52 and 54 have also been amended to better distinguish over Sorenson. Similar arguments apply to independent claim 52 (*counter coupled to count a number of data transitions of a received data stream that occur over multiple cycles of an oscillator used to generate a recovered clock from the received data stream and generate a count value indicative thereof*) and to claim 54 (*means for counting a number of data transitions in a received data stream that occur over multiple cycles of an oscillator used to generate a*

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recovered clock from the received data stream). Accordingly, applicants submit that claims 52 and 54, and all claims dependent thereon, distinguish over the references of record.

In summary, claims 1-49 and 51-55 are in the case. All claims are believed to be allowable over the art of record, and a Notice of Allowance to that effect is respectfully solicited. Nonetheless, if any issues remain that could be more efficiently handled by telephone, the Examiner is requested to call the undersigned at the number listed below.

Respectfully submitted,

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